

ACesso NA MEMÓRIA DE VÍDEO DELA CPU (modo ALFA BOX25)

14MH₂

pin09 CI-1

pin08 CI-1

pin05 CI-1

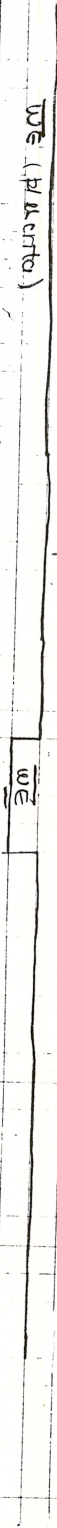
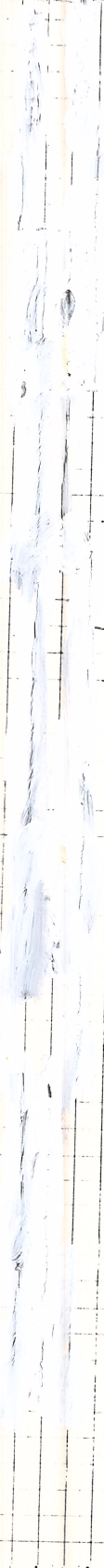
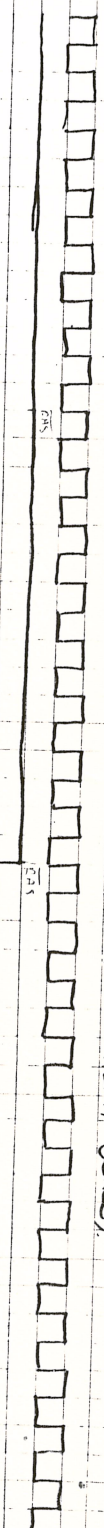
pin06 CI-1

pin08 CI6t

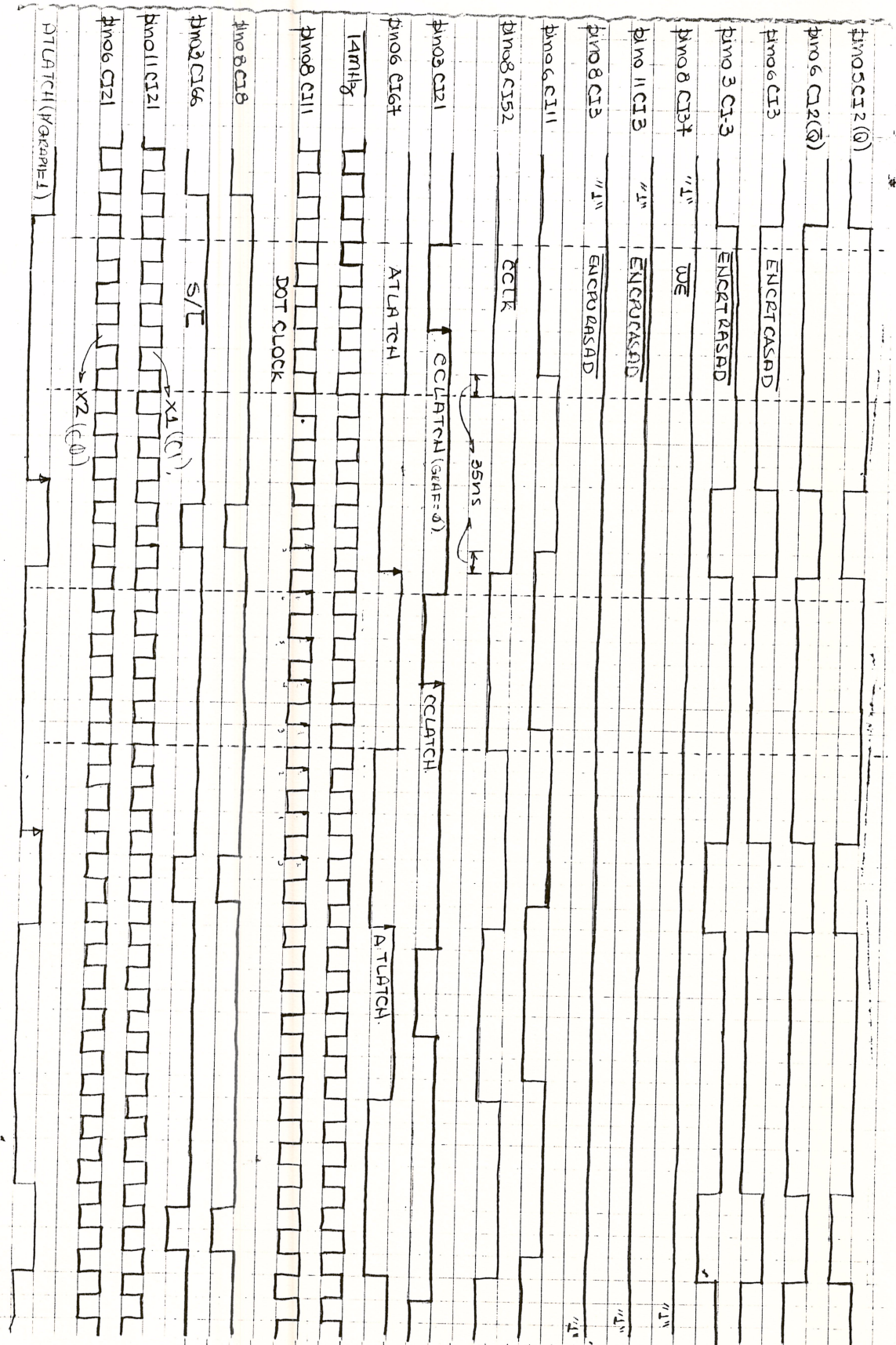
pin011 CI3

pin08 CI3

pin08 CI3t



WE

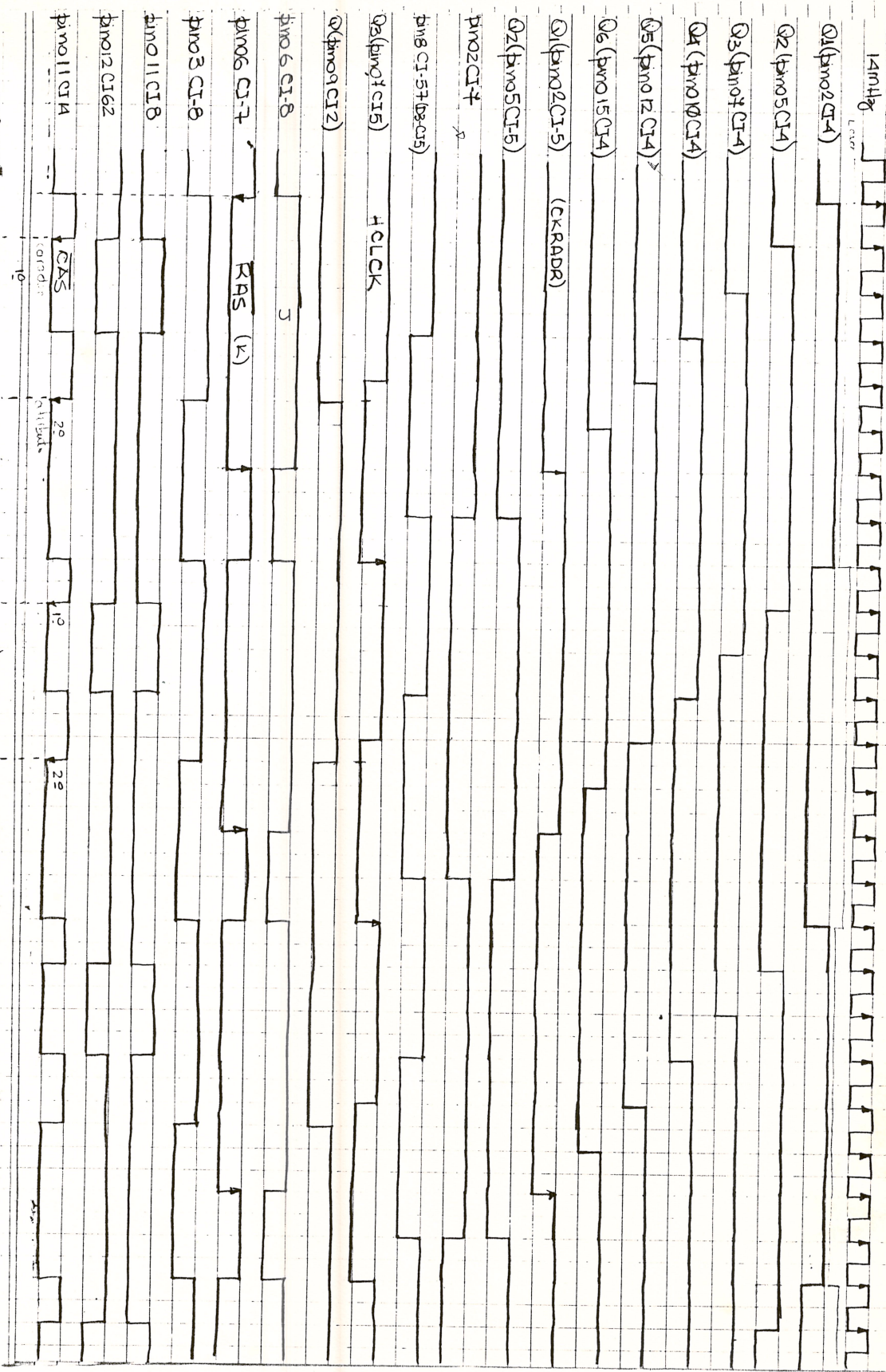


RESET

SECUENCIA DE CONTROL VIDEO GRAFICO 17000 PC (HRES=1 → BOX25 ALFANUMERICO)

14MHz

70ns



14MHz

pin09 CI-1

pin08 CI-1

pin05 CI-1

pin06 CI-1

pin08 CI67

pin011 CI3

pin08 CI-3

pin06 CI-3

pin03 CI-3

pin08 CI34

ACESSO NA MEMÓRIA DE VÍDEO DEUA CPU (MODO ALFA BOX25)



RAS

TOREADY ou XACC

(circundado de 6045)

(acesso pela CPU)

ENCPUERASAD

CREAD

180AS

200AS

ENCPUERASAD

180AS

ENCRTCASAD

ENCRTCASAD

WE (ponto)

WE



HSYNC

20µs 44µs

64µs

RESET

SECUENCIAL - CONTR VIDEO GRAFICO 170000 PC (HRES = 1 -> 80x25 ALFANUMÉRICO)

10ms

