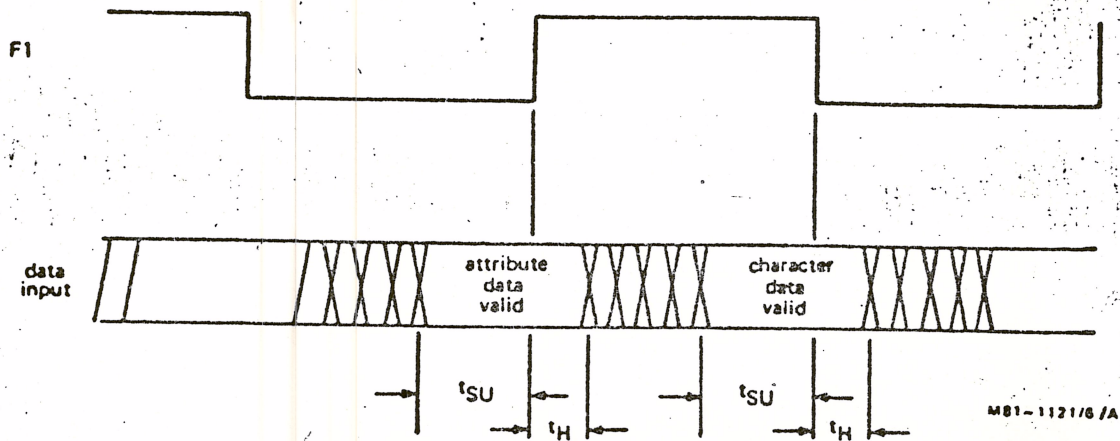




all timings measured at the 1.5V level

Fig.6 Input data timing with respect to F1

