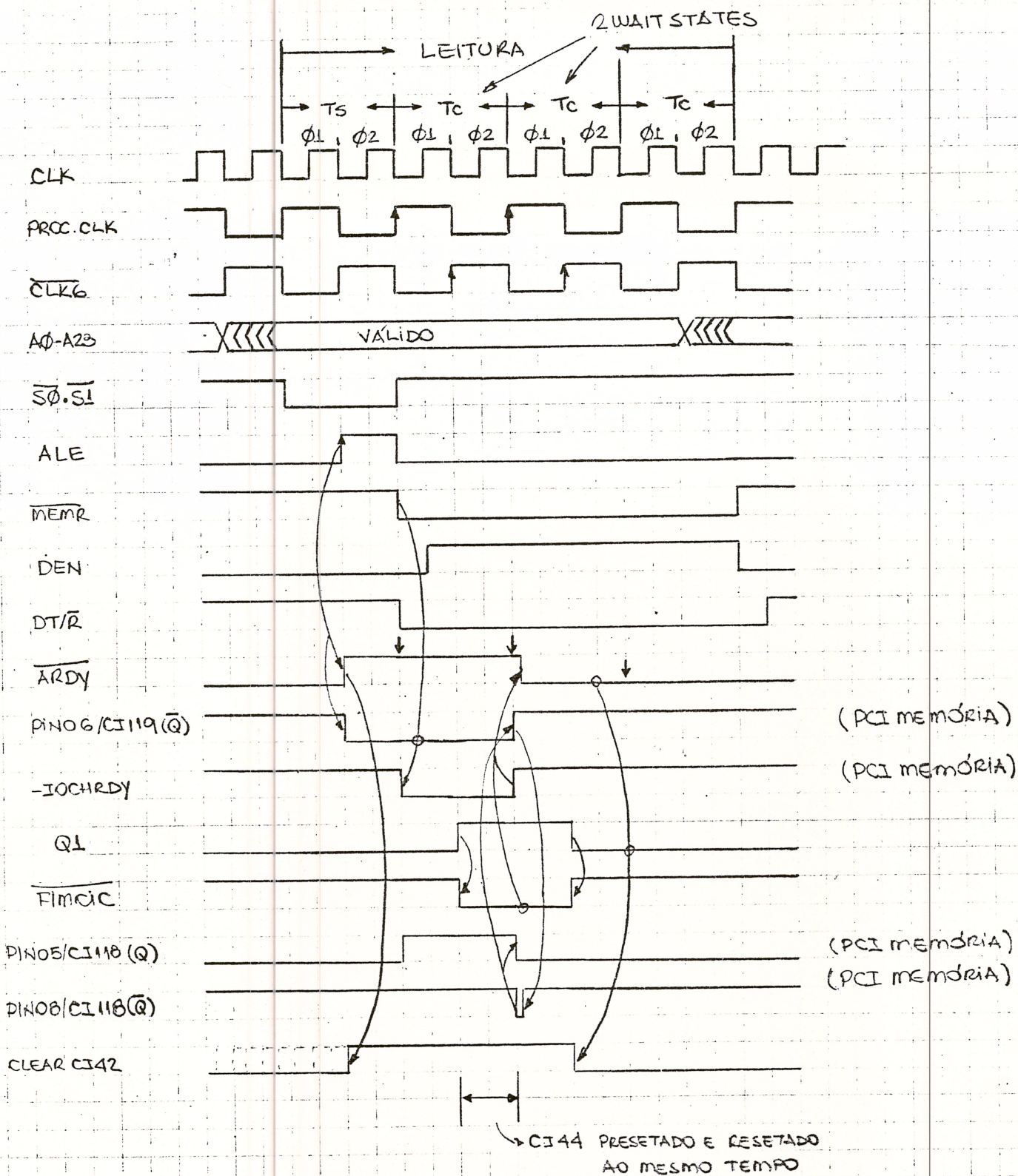
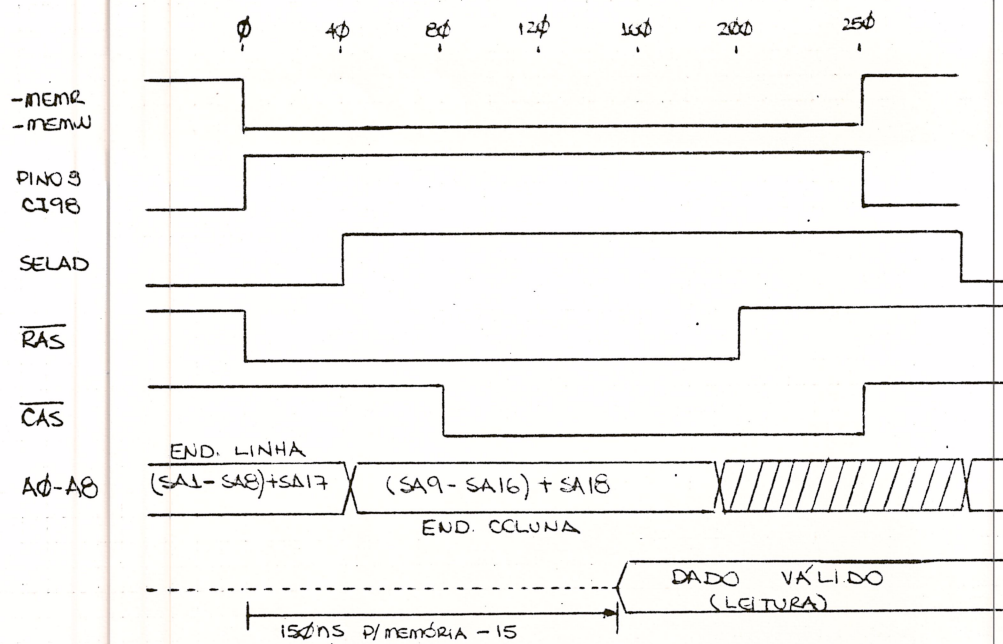




OPERAÇÃO DE ~~16 BITS~~ 8 BITS: LEITURA DE 16 BITS NA MEMÓRIA EPROM DE 16 BITS (8/10)

CPU 8/10 MHz → MEM 8/10 MHz

(JP 05 e 12 em ON ⇒ FREQUÊNCIA)



DREFRESH

-MEMR

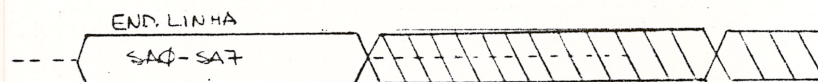
PINO 3
CI98

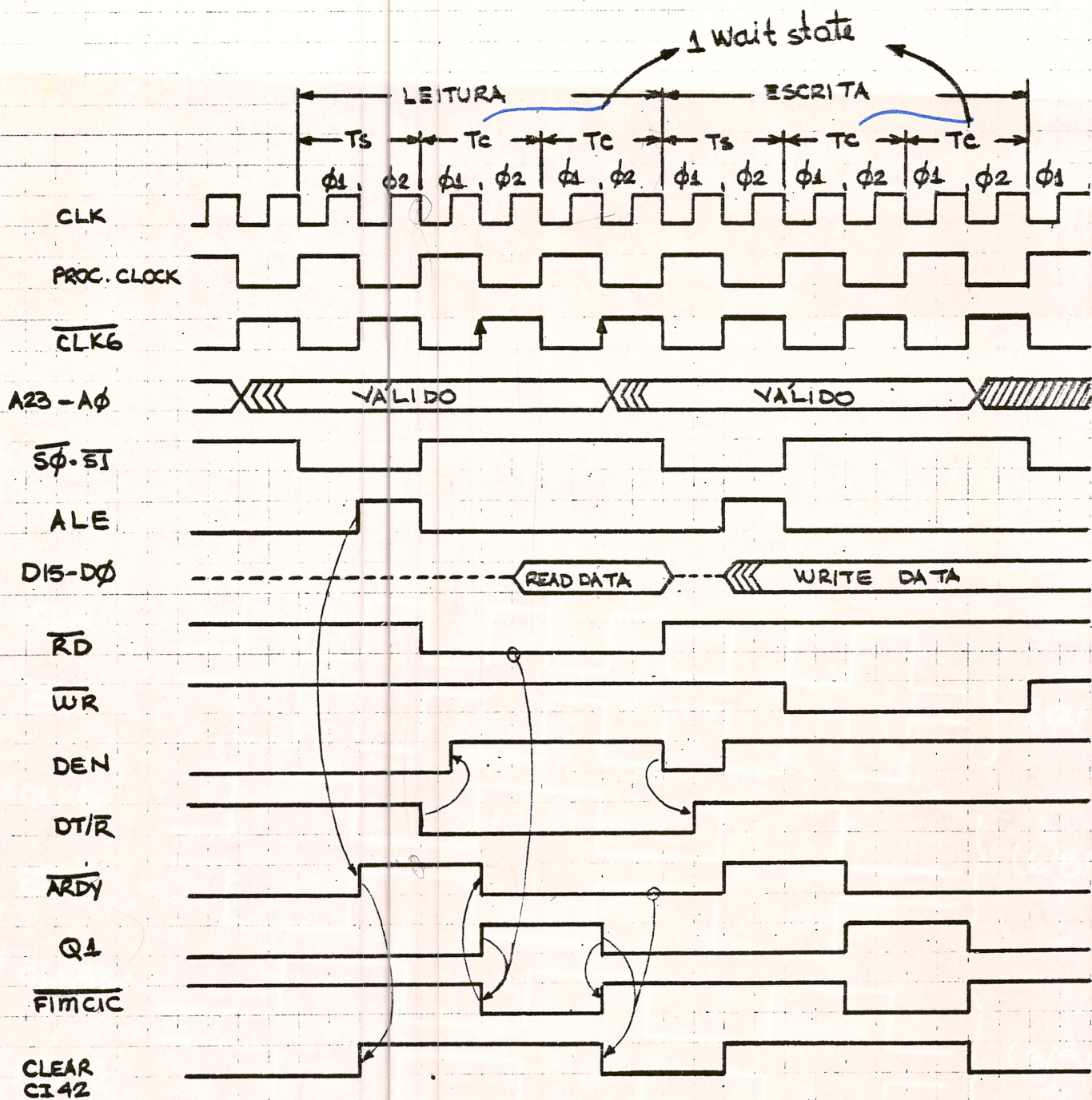
SELAD

RAS

CAS

A0-A7



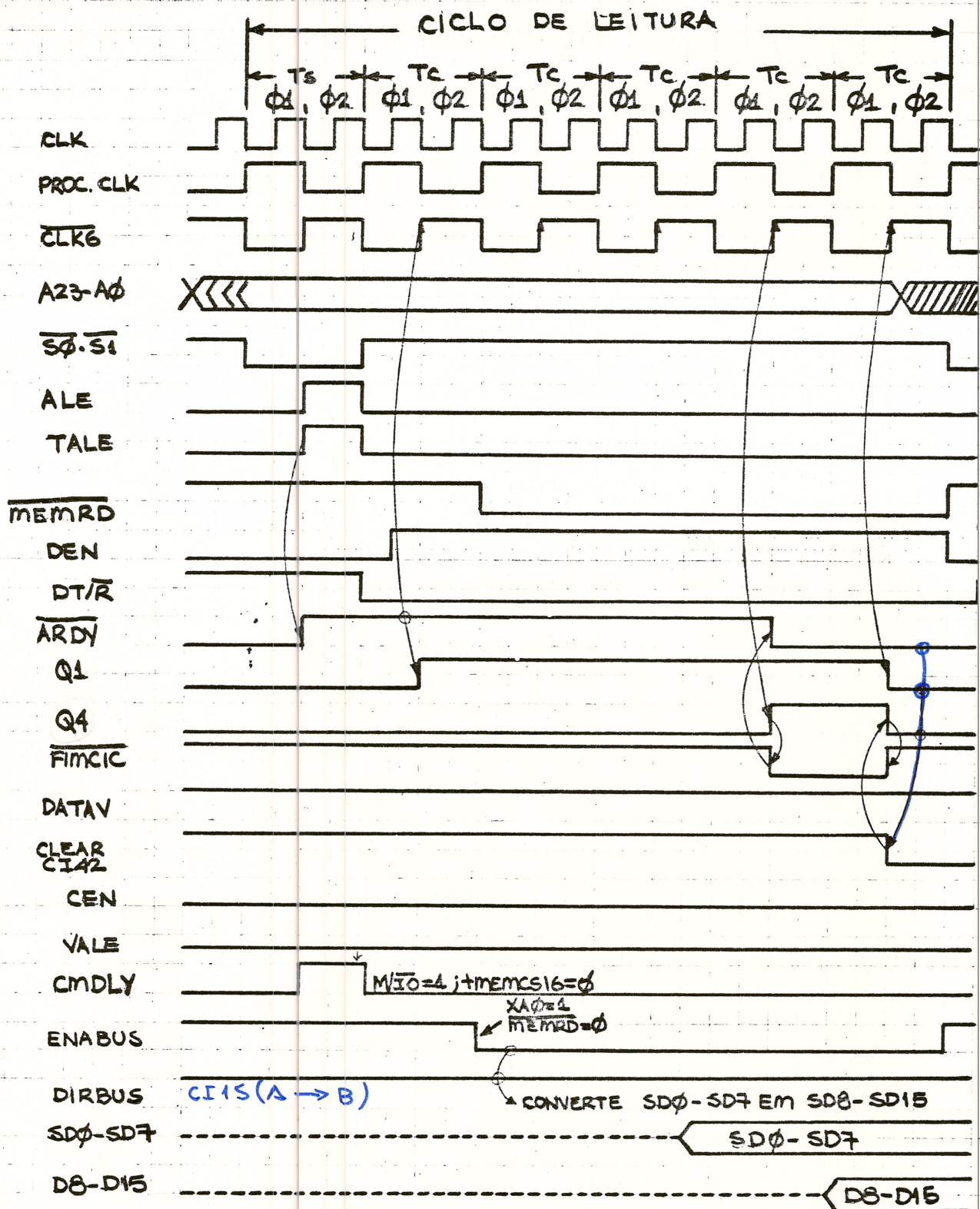


OPERAÇÃO DE 16 BITS EM DISPOSITIVO DE 16 BITS.

$A0 = XA0 = LSA0 = \emptyset$

$BHE = \emptyset$

(1 WAIT STATE)



OPERAÇÃO DE LEITURA DE 8 BITS NA MEMÓRIA DE 8 BITS.
(ENDEREÇO ÍMPAR: $A0=1$ e $BHE=\phi$).

(4 WAIT STATES)

