

TDA 180F

TV HORIZONTAL PROCESSOR

- Noise gated horizontal sync separator.
- Noise gated vertical sync separator.
- Horizontal oscillator with frequency range limiter.
- Phase comparator between sync pulses and oscillator pulses (PLL).
- Phase comparator between flyback pulses and oscillator pulses (PLL).
- Loop gain and time constant switching (VCR).
- Composite blanking and key pulse generator.
- Protection circuits.
- Output stages with high current capability.

V ₅	Supply voltage (pin 1)	15	V
V ₂	Voltage at pin 2	18	V
V ₄	Voltage at pin 4	V ₅	
V ₈	Voltage at pin 8	V ₃	
V ₉	Voltage at pin 9	{ -6	V
V ₁₁	Voltage at pin 11	{ +6	V
I ₂	Pin 2 peak current	-6	
I ₃	Pin 3 peak current	1	A
I ₆	Pin 6 current	0.5	A
I ₇	Pin 7 current	30	mA
I ₁₀	Pin 10 current	20	mA
P _{tot}	Total power dissipation at T _{amb} ≤ 70°C	30	mA
T ₅₀ , T ₁	Storage and junction temperature	1	W
		-40 to 150	°C

MECHANICAL DATA

Technical drawing of a rectangular metal part. The drawing includes a top view and a cross-sectional view. The top view shows a rectangle with a width of 20 mm and a length of 17.78 mm. The cross-sectional view shows a rectangular profile with a height of 0.45 mm and a width of 0.30 mm. The drawing is labeled with dimensions and a scale of 1:1.

Block diagram of the 5446 receiver showing 16 numbered input/output points and their functions:

- 16: GND
- 15: OSC CONTROL
- 14: OSCILLATOR
- 13: CONT CURRENT
- 12: SWITCH
- 11: TIME CONSTANT
- 10: CONCORDANCE DETECTOR
- 9: VERTICAL SYNC SEPARATOR IN
- 8: VERTICAL SYNC SEPARATOR IN
- 7: VERTICAL SYNC OUTPUT
- 6: KEY AND BLANK
- 5: FLUXBACK INPUT
- 4: PHASE SHIFTER
- 3: FILTER
- 2: PROTECTION CIRCUIT INPUT
- 1: OUTPUT *
- 0: SUPPLY VOLTAGE

[illegible]