

TDA 1180F

LINEAR INTEGRATED CIRCUIT

TDA 1180F

TV HORIZONTAL PROCESSOR

The TDA 1180F is a horizontal processor circuit for b.w. and colour television receiver. It is a monolithic integrated circuit encapsulated in 16-lead dual in-line plastic package. The TDA 1180F is a modified version of the TDA 1180P particularly suited for France standard and Bi-standard application. It combines the following functions:

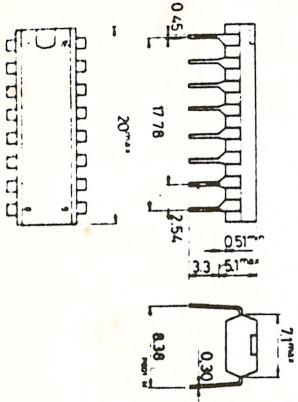
- Noise gated horizontal sync separator.
- Horizontal oscillator with frequency range limiter.
- Phase comparator between sync pulses and oscillator pulses (PLL).
- Phase comparator between flyback pulses and oscillator pulses (PLL).
- Loop gain and time constant switching (VCR).
- Composite blanking and key pulse generator.
- Protection circuits.
- Output stages with high current capability.

ABSOLUTE MAXIMUM RATINGS

V _s	Supply voltage (pin 1)	15	V
V ₂	Voltage at pin 2	18	V
V ₄	Voltage at pin 4	-6	V
V ₈	Voltage at pin 8	+6	V
V ₉	Voltage at pin 9	-6	V
V ₁₁	Voltage at pin 11	1	A
I ₂	Pin 2 peak current	0.5	mA
I ₃	Pin 3 peak current	30	mA
I ₆	Pin 6 current	20	mA
I ₇	Pin 7 current	30	mA
I ₁₀	Pin 10 current	1	W
P _{tot.}	Total power dissipation at T _{amb} <= 70°C		
T _{stg.} , T _j	Storage and junction temperature	-40 to 150	°C

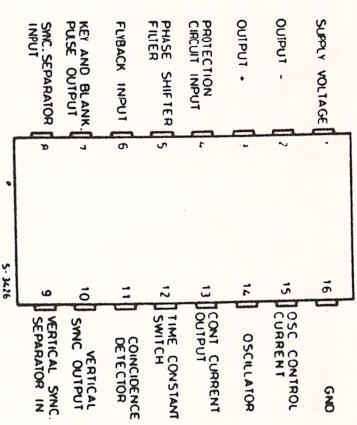
ORDERING NUMBER: TDA 1180F

MECHANICAL DATA



Dimensions in mm

CONNECTION DIAGRAM



BLOCK DIAGRAM

