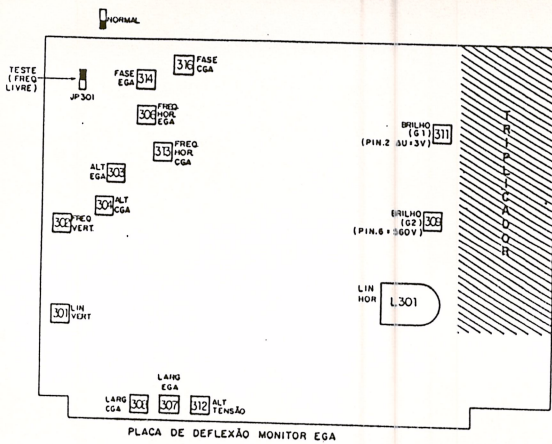




DIP DS02							CONFIGURACAO	NORMAL
CH1	CH2	CH3	CH4	CH5	CH6			
ON	ON	ON	OFF	-	-		ENHANCED RGB	
ON	OFF	OFF	ON	-	-		CGA 40 X 25 COLORIDO	
OFF	ON	ON	OFF	-	-		EGA	**
OFF	OFF	ON	OFF	-	-		MDA (MONOCROMATICO)	
OFF	OFF	OFF	ON	-	-		CGA 80 X 25 COLORIDO	
-	-	-	-	ON	-		PERMITE O AUTO-SWITCH	**
-	-	-	-	OFF	-		NAO PERMITE O AUTO-SWITCH	
-	-	-	-	-	ON		INTERFACE EGA DA PCI HABILITADA	**
-	-	-	-	-	OFF		INTERF. EGA DA PCI DESABILITADA	

15TA002

DIP DS01					
CHAVE	POSICAO		FUNCAO		
CH01	ON		AUTO-TESTE CONTINUO		
	OFF		OPERACAO NORMAL		
CH02	ON		COPROCESSADOR 8087 NAO INSTALADO		
	OFF		COPROCESSADOR 8087 INSTALADO		
CH03 e CH04	*	*	SEM FUNCAO		
CH05 e CH06	ON	ON	INTERFACE DE VIDEO EGA		
	OFF	ON	INTERFACE DE VIDEO CGA 40 X 25		
	ON	OFF	INTERFACE DE VIDEO CGA 80 X 25		
	OFF	OFF	INT. DE VIDEO MDA (MONOCR. IBM)		
CH07 e CH08	ON	ON	1 UNIDADE FLOPPY 5 1/4		
	OFF	ON	2 UNIDADES FLOPPY 5 1/4		
	ON	OFF	3 UNIDADES FLOPPY 5 1/4		
	OFF	OFF	4 UNIDADES FLOPPY 5 1/4		

15TA003A

ESTRAPE	POSICAO	FUNCAO	NORMAL
JP01/02	1 - ON	NAO INSERE CICLO DE WAIT	ON
	OFF	INSERE UM CICLO DE WAIT EM 8 MHz NA RAM	
	2 - ON	VELOCIDADE APOS RESET = 8 MHz	ON
	OFF	VELOCIDADE APOS RESET = 4,77 MHz	
	3 - ON	3 BANCOS DE MEMORIA	ON
	OFF	2 BANCOS DE MEMORIA	
JP03	4 - ON	64 k de memoria de video	OFF
	OFF	128 k de memoria de video	
	5 - ON	INTERFACE SERIAL PRIMARIA DESABILITADA	OFF
	OFF	INTERFACE SERIAL PRIMARIA HABILITADA	
	6 - ON	INTERFACE SERIAL SECUNDARIA DESABILITADA	OFF
	OFF	INTERFACE SERIAL SECUNDARIA HABILITADA	
JP04	1 - 2	INT. SER. SEC. CONO LOOP ITAUEC	2-3
	2 - 3	INT. SER. SEC. CONO RS232C	
JP05	1 - 2	INT. SER. PRIM. CONO LOOP ITAUEC	2-3
	2 - 3	INT. SER. PRIM. CONO RS232C	
JP06	1 - 2	INT. SER. SEC. CONO LOOP ITAUEC	1-2
	2 - 3	INT. SER. PRIM. CONO RS232C	
JP07	ON	ALIMENTA O RTC	ON
	OFF	CORTA A ALIMENTACAO DA BATERIA P/O RTC	
JP08	1 - 2	EPROM DA EGA = 27128	1-2
	2 - 3	EPROM DA EGA = 27256	
JP09	1 - 2	END. DE E/S MAP. EM 2XX	2-3
	2 - 3	END. DE E/S MAP. EM 3XX	
JP10	1 - 2	TAMANHO DA BIOS EGA = 32 k	2-3
	2 - 3	TAMANHO DA BIOS EGA = 16 k	
JP11		RESERVADO (NAO E MONTADO)	
JP12	ON	PRE-CONP. ESCR. NO FLOPPY = 180' AS	OFF
	OFF	PRE-CONP. ESCR. NO FLOPPY = 125 AS	
JP13	ON	ROTOR "SPINDLE" DO DRIVE TEN 2 VELOC.	
	OFF	ROTOR "SPINDLE" DO DRIVE TEN 1 VELOC.	
PTB/PT9		IMPLEMENTACAO FUTURA	ON

15TA004

TIPO				CAPACIDADE FORMATA DA	FABRICANTE	MODELO
D8 D4	D7 D3	D6 D2	D5 D1			
ON	ON	ON	ON	10,7 M	MULTIDIGIT/ FLEXIDISK	DW1011/ BR412
ON	ON	ON	OFF	10,7 M	ELEBRA	W311
ON	ON	OFF	ON	21,3 M	QUALITRON/ ITAUEC/BASF MICROLAB/ SEAGATE	QU520/ 6188R3/DFW 5025/ST125/ ST225
ON	ON	OFF	OFF	21,3 M	ELEBRA/ MULTIDIGIT	DU2061/ VINTEM
ON	OFF	ON	ON	21,3 M	FLEXIDISK	BR425
ON	OFF	ON	OFF	24,3 M	ELEBRA	W530-S
ON	OFF	OFF	ON	40,3 M	ELEBRA	W540-S
ON	OFF	OFF	OFF	42,9 M	MICROLAB	DFW5050
OFF	ON	ON	ON	32,0 M	SEAGATE	ST138
OFF	ON	ON	OFF	42,0 M	SEAGATE	ST251/251-1
OFF	ON	OFF	ON	31,0 M	SEAGATE	ST4038
OFF	ON	OFF	OFF	42,0 M	SEAGATE	ST4051
OFF	OFF	ON	ON	44,0 M	SEAGATE	ST4053

PLACA CONTROLADORA DE WINCHESTER			NOTA:	1	2	3
ESTRAPE	POSICAO	FUNCAO				
JP01	ON	HABILITA A PLACA				ON
	OFF	DESABILITA A PLACA				
JP02	1 - 2	CONECTA O SINAL HS3 COM O PINO 2 DO CH01				2-3
	2 - 3	CONECTA O SINAL RUC COM O PINO 2 DO CH01				

15TA005

NOTA: JP02 EM 1-2 PARA WINCH. COM MAIS DE 8 CABECAS.