

Itautec

I1----	1	20	----	VCC
I2----	2	19	----	01
I3----	3	18	----	02
I4----	4	17	----	03
I5----	5	16	----	04
I6----	6	15	----	I13
I7----	7	14	----	I12
I8----	8	13	----	I11
I9----	9	12	----	05
GND----	10	11	----	I10

Logic NAME is CI78 (PLACA EMS)

The device is a DMPAL16L8

series 20 medium device

with active low outputs

$$\sqrt{IF(VCC)/01 = I4*/I5*I9*/I10*I11}$$

$$\sqrt{IF(VCC)/02 = I4*/I5*I9*I10*I11}$$

$$\sqrt{IF(VCC)/03 = /I1*I3*/I5*I6*/I7*/I8*I9*I10*I11*/I12*I13}$$

$$\sqrt{IF(VCC)/04 = /I1*I3*/I5*I6*/I7*/I8*I9*I10*I11*I12*I13}$$

$$\sqrt{IF(VCC)/05 = I3*/I7*/I8*I9*/I10*I11*/I6 + I3*I6*/I7*/I8*I9*/I10*I11*/I13 + I3*I6*/I7*/I8*I9*I10*I11*I13}$$

I1----	1	20 ----VCC
I2----	2	19 ----01
I3----	3	18 ----02
I4----	4	17 ----03
I5----	5	16 ----04
I6----	6	15 ----I13
I7----	7	14 ----I12
I8----	8	13 ----I11
I9----	9	12 ----05
GND----	10	11 ----I10

Logic NAME is CI79 (PLACA EMS)

The device is a DMPAL16L8
series 20 medium device
with active low outputs

$$\checkmark \text{ IF(VCC)/01} = /I4 + /I5 + /02 + /03$$

$$\checkmark \text{ IF(VCC)/02} = /I3*/I6*/I7*/I8*I9*I10*I11*I12*/I13 \\ + /I3*I6*I7*I8*I9*I10*I11*I12*/I13$$

$$\checkmark \text{ IF(VCC)/03} = /I6*I7*/I8*I9*I10*I11*I12*/I13$$

$$\checkmark \text{ IF(VCC)/04} = /I2*/I4 \\ + /I2*/I5 \\ + /I1*/02 \\ + /I1*/03$$

$$\checkmark \text{ IF(VCC)/05} = /I9*I10*/I11*I12 \\ + /I8*I9*I10*/I11*I12$$

Itautec

I1----	1	20 ----VCC
I2----	2	19 ----01
I3----	3	18 ----02
I4----	4	17 ----03
I5----	5	16 ----04
I6----	6	15 ----05
I7----	7	14 ----06
I8----	8	13 ----07
I9----	9	12 ----08
GND----	10	11 ----I10

Logic NAME is CI80

(PLACA EMS)

The device is a DMPAL16L8

series 20 medium device

with active low outputs

REPRODUCED FROM

$$\checkmark IF(VCC)/01 = I1*/I3*/I5*I6*/I7*/I8*/I9*03$$

$$\checkmark IF(VCC)/02 = /I3*/I5*/I9 \\ + /I4*/I5*/I9 \\ + I2*I3*I4 \\ + I2*I9$$

$$\checkmark IF(VCC)/03 = I1*/I3*/I5*/I6*/I9 \\ + I2*I3*I4 \\ + I2*I9$$

$$\checkmark IF(VCC)/04 = I1*/I2*/I4*/I5*/I6*/I9 \\ + I2*/I4*/I5*/I6*/I9*03$$

$$\checkmark IF(VCC)/05 = /I3*/I5*/I6*/I9 \\ + /I4*/I5*/I6*/I9$$

$$\checkmark IF(VCC)/06 = /I3*/I5*/I9 \\ + /04 \\ + I1*/I4*/I5*I6*/I9*03$$

$$\checkmark IF(VCC)/07 = I1*/I3*/I5*I6*I7*/I8*/I9*03$$

$$\checkmark IF(VCC)/08 = /I4*/I5*I6*/I7*/I8*/I9$$

Itautec

I1----	1	20 ----VCC
I2----	2	19 ----01
I3----	3	18 ----02
I4----	4	17 ----03
I5----	5	16 ----04
I6----	6	15 ----05
I7----	7	14 ----I/07
I8----	8	13 ----I11
I9----	9	12 ----06
GND----	10	11 ----I10

Logic NAME is CI81

(PLACA EMS)

The device is a DMPAL16L8
series 20 medium device
with active low outputs

REPRODUCED BY THE MANUFACTURER

$$\rightarrow IF(VCC)/01 = I3*/04$$

$$\rightarrow IF(VCC)/02 = I1*I6*I7*I8*/I10*I11 + I1*/I5*I6*/I7*I8*/I10*I11$$

$$\checkmark IF(VCC)/03 = I1*I6*I7*I8*/I10*I11 + I1*/I5*I6*/I7*I8*/I10*I11$$

$$\rightarrow IF(VCC)/04 = I1*I6*I7*I8*/I9*I11 + I1*/I5*I6*/I7*I8*/I9*I11$$

$$\checkmark IF(VCC)/05 = I1*I6*I7*I8*/I9*I11 + I1*/I5*I6*/I7*I8*/I9*I11 + I1*I6*I7*I8*/I10*I11 + I1*/I5*I6*/I7*I8*/I10*I11$$

$$\rightarrow \checkmark IF(I4)/06 = I4$$

I1----	1	20	----	VCC
I2----	2	19	----	01
I3----	3	18	----	02
I4----	4	17	----	03
I5----	5	16	----	04
I6----	6	15	----	05
I7----	7	14	----	06
I8----	8	13	----	07
I9----	9	12	----	08
GND----	10	11	----	I10

Logic NAME is CI82

(PLACA EMS)

The device is a DMPAL16L8

series 20 medium device

with active low outputs

$$\checkmark \quad IF(VCC)/01 = /I6*/I9 + /I1*/I2*/I3*I4*I7*I8*/I9 + /I1*/I2*/I3*I4*/I5*/I7*I8*/I9 + /I1*/I2*/I3*I4*I7*I8*/I10 + /I1*/I2*/I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/02 = /I6*/I9 + I1*/I2*/I3*I4*I7*I8*/I9 + I1*/I2*/I3*I4*/I5*/I7*I8*/I9 + I1*/I2*/I3*I4*I7*I8*/I10 + I1*/I2*/I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/03 = /I6*/I9 + /I1*I2*/I3*I4*I7*I8*/I9 + /I1*I2*/I3*I4*/I5*/I7*I8*/I9 + /I1*I2*/I3*I4*I7*I8*/I10 + /I1*I2*/I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/04 = /I6*/I9 + I1*I2*/I3*I4*I7*I8*/I9 + I1*I2*/I3*I4*/I5*/I7*I8*/I9 + I1*I2*/I3*I4*I7*I8*/I10 + I1*I2*/I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/05 = /I6*/I9 + /I1*/I2*I3*I4*I7*I8*/I9 + /I1*/I2*I3*I4*/I5*/I7*I8*/I9 + /I1*/I2*I3*I4*I7*I8*/I10 + /I1*/I2*I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/06 = /I6*/I9 + I1*/I2*I3*I4*I7*I8*/I9 + I1*/I2*I3*I4*/I5*/I7*I8*/I9 + I1*/I2*I3*I4*I7*I8*/I10 + I1*/I2*I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/07 = /I6*/I9 + /I1*I2*I3*I4*I7*I8*/I9 + /I1*I2*I3*I4*/I5*/I7*I8*/I9 + /I1*I2*I3*I4*I7*I8*/I10 + /I1*I2*I3*I4*/I5*/I7*I8*/I10$$

$$\checkmark \quad IF(VCC)/08 = /I6*/I9 + I1*I2*I3*I4*I7*I8*/I9 + I1*I2*I3*I4*/I5*/I7*I8*/I9 + I1*I2*I3*I4*I7*I8*/I10 + I1*I2*I3*I4*/I5*/I7*I8*/I10$$